# WILTEK, INC.
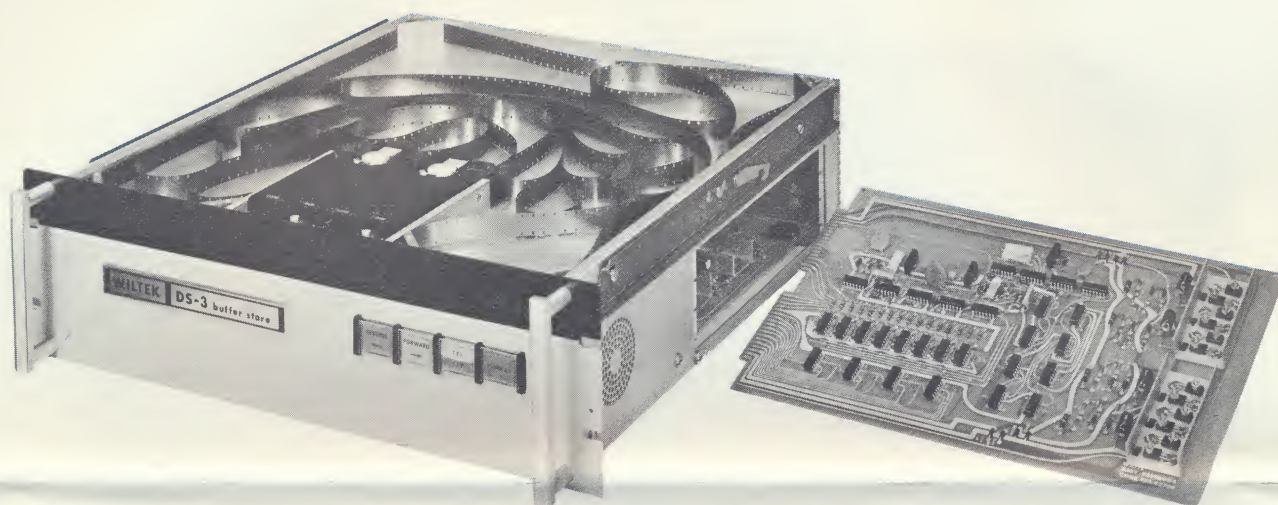
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## DESCRIPTION

The WILTEK Model DS-3 Buffer Store combines two independent tape mechanisms in a single unit. Both tape mechanisms operate with a common tape loop which is stored in a random bin in a horizontal compartment on top of the chassis. Each mechanism is capable of stepping the tape asynchronously up to 333 characters per second in either direction. Tape sensors prevent damage should either of the mechanisms exert undue tension on the tape.

One of the heads writes on the tape while the other reads the data thereby providing simultaneous input and output.

The use of the endless loop eliminates the need for replacing or rewinding the tape.

Two printed circuit cards, accessible from the right hand side, contain the electronics. One board serves the write transport and the other the read transport. Maximum use has been made of integrated circuit technology to reduce the size and cost of this unit. Space is available for a third board to accommodate special interface requirements.

## SPECIFICATIONS

| | |
|---|---|
| RECORDING MEDIUM | 16 mm double sprocketed magnetic tape |
| READING AND WRITING SPEED | Asynchronous up to 333 characters per second |
| RECORDING DENSITY | 1000 characters per foot<br>25 characters per frame |
| RECORDING CHANNELS INPUT | Up to 8<br>8 data lines in parallel*<br>Serial input lines* — Data and complement<br>Shift clock line*<br>Strobe line — Indicates cycle on transitions from "0" to "1"<br>Forward/Reverse control line —<br>      "1" — Forward<br>      "0" — Reverse |
| OUTPUT | 8 data lines and their complements<br>Serial output lines* — Data and complement<br>Shift clock line*<br>Strobe line<br>Forward/Reverse control line |
| SIZE | 5¼″ × 19″ × 22″ |
| WEIGHT | 40 lbs. |
| POWER REQUIREMENT | 115V, 50 - 60 Hz, 2 a.<br>Input/Output Logic 0 = 0 ± 1V<br>1 = +6 ± 1V |

*Input and output registers are wired to be used as Shift Register if desired.

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MANUFACTURERS OF ELECTRONIC COMMUNICATIONS AND DATA HANDLING EQUIPMENT

## WILTEK Model DS-3 BUFFER STORE
Fast, accurate reception, storage and transmission of binary coded data

### FEATURES

- Low Cost

- High Storage Capacity — 50,000 characters

- Incremental to 333 characters per second Input and Output

- Small Size — 5¼ inches high

- Integrated Circuit Reliability

- Long Tape Life

- Bi-directional Read and Write

- Self contained special interfacing available

- Rack or desk mounting

### TYPICAL APPLICATIONS

**1. Buffered Rate Changing.**
   a. High-speed data in, low-speed data out, with speed difference taken up by storage.
   b. Low-speed data in, high-speed data out, with speed difference taken up by outputting data in bursts.

   The two combinations can be used with switching networks to increase communication line efficiency.

**2. Data Accumulation.**

   Data can be accumulated at a receiving terminal over an extended period of time and upon signal transmitted at high-speed into a computer or retransmission equipment.

**3. Editing.**

   Data can be entered through the Write head and with addition of appropriate logic, the Read head can scan the tape to perform editing and selection of paragraphs.

**4. Message Sorting/Routing.**

   Temporary storage for incoming and outgoing traffic in store and forward message handling systems.

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